

FIG.1

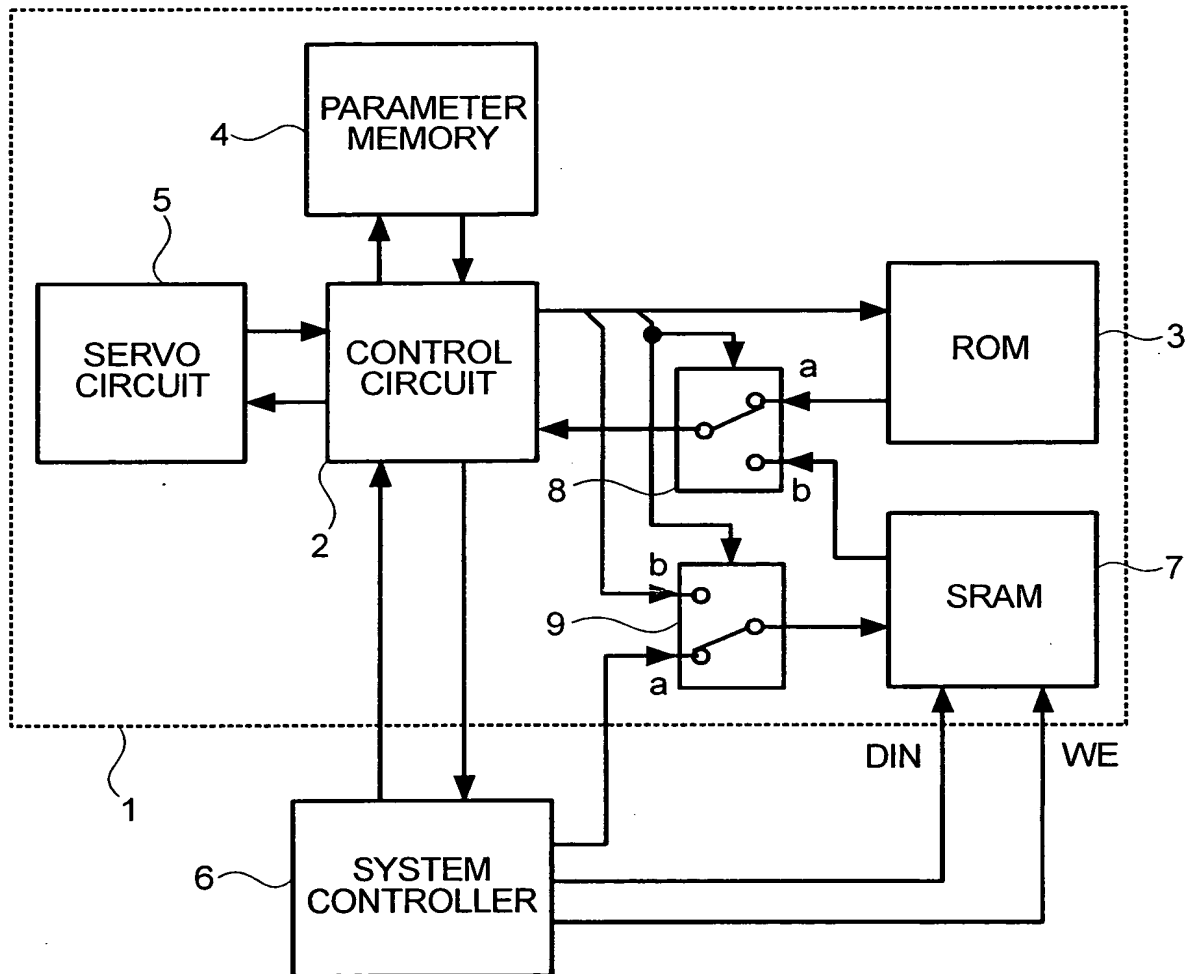
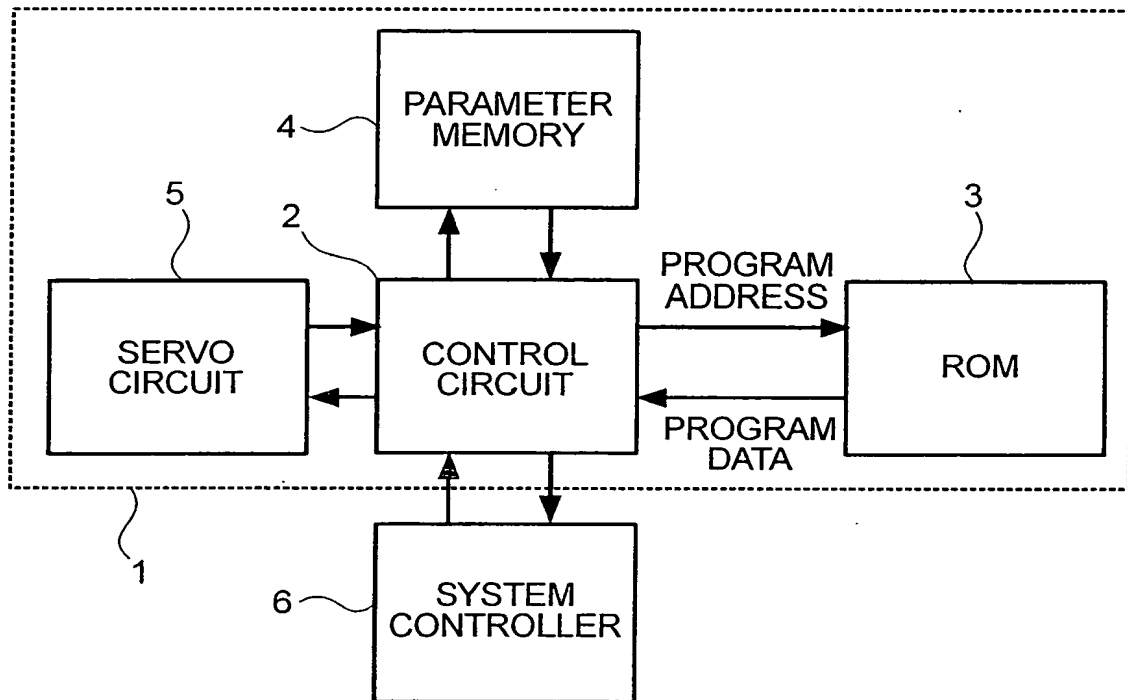


FIG.2



(PRIOR ART)

FIG.3

| DECIMAL ADDRESS | HEXADECIMAL ADDRESS | KIND OF PROGRAM DATA |
|-----------------|---------------------|----------------------|
| 0 | 0 0 0 0 | ROM OUTPUT |
| 1 | 0 0 0 1 | |
| ⋮ | ⋮ | |
| 8 1 9 0 | 1 F F E | |
| 8 1 9 1 | 1 F F F | |
| 8 1 9 2 | 2 0 0 0 | SRAM OUTPUT |
| 8 1 9 3 | 2 0 0 1 | |
| ⋮ | ⋮ | |
| 8 7 0 2 | 2 1 F E | |
| 8 7 0 3 | 2 1 F F | |

FIG.4

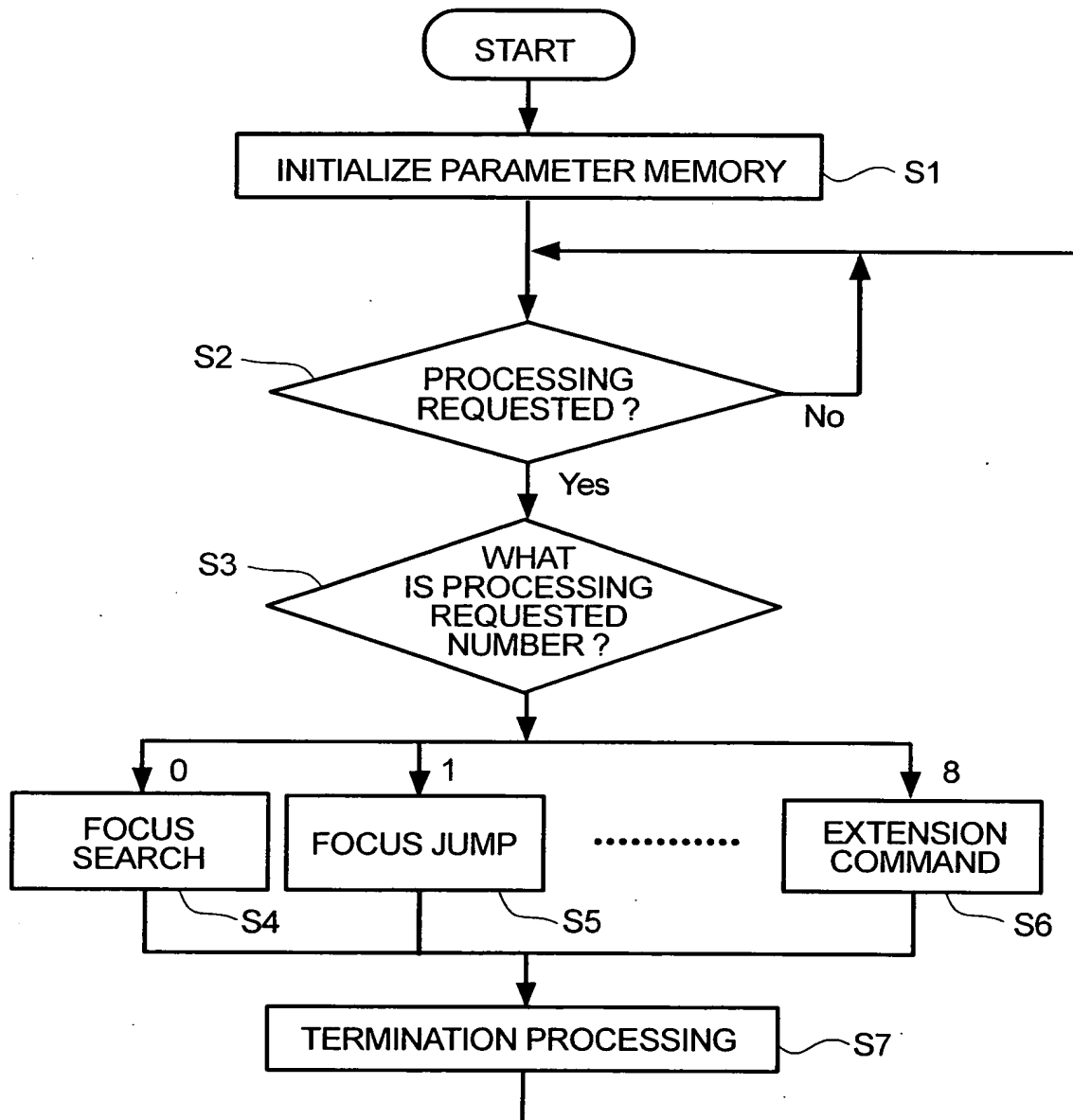


FIG.5

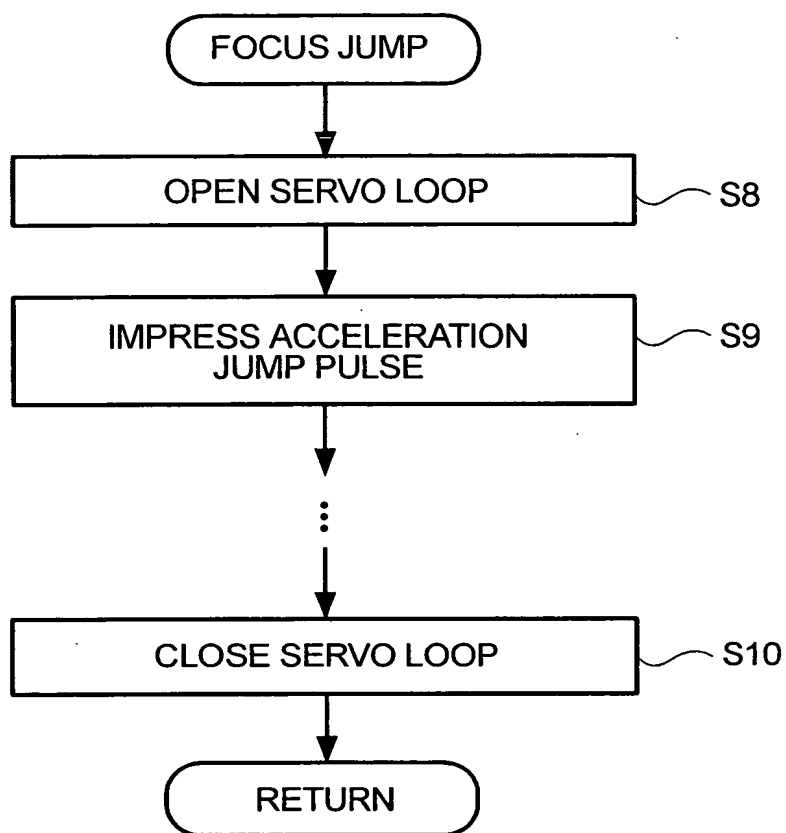


FIG.6

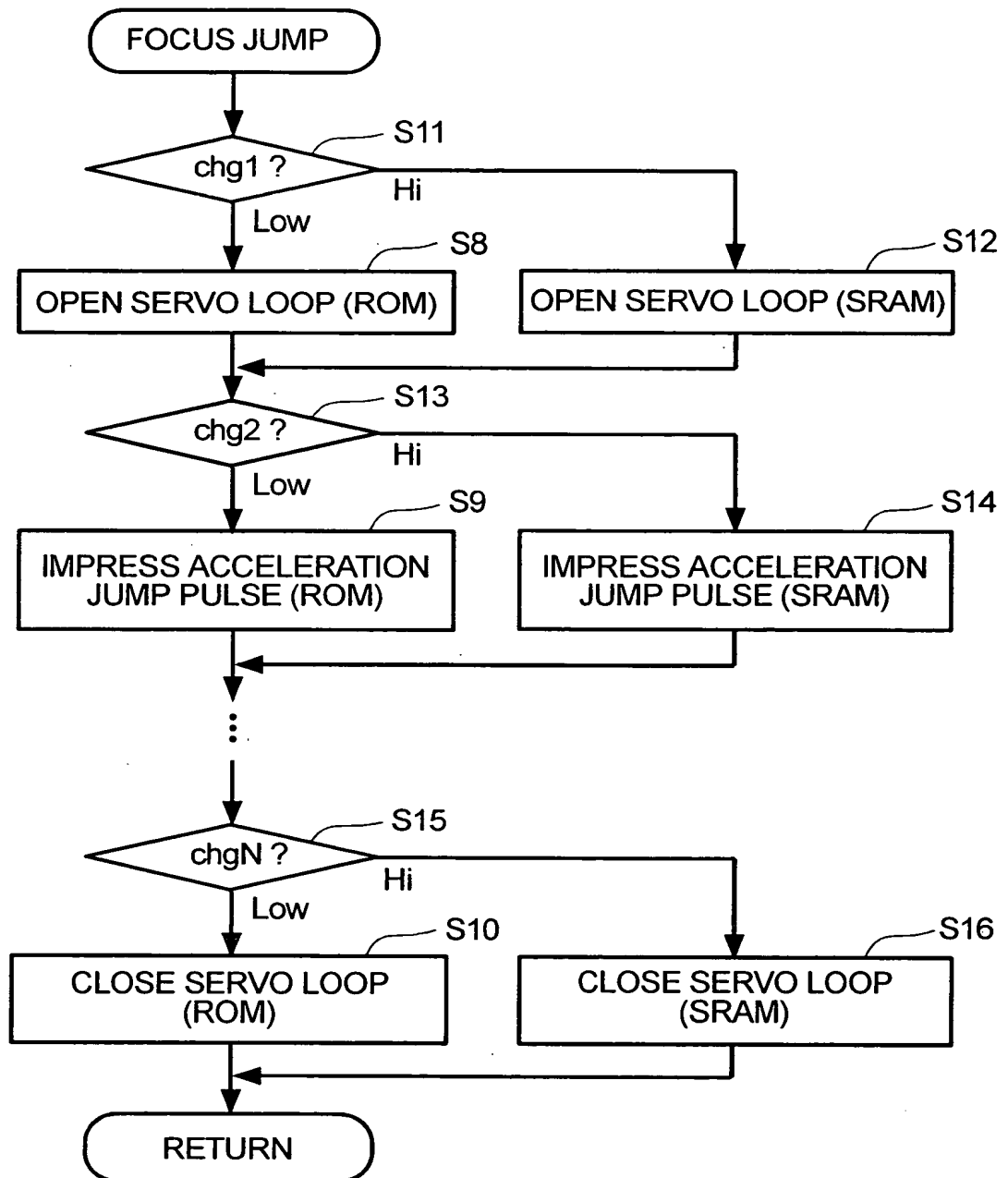


FIG.7

